

RonView Display

Ron View 龙威显示科技





DISPLAY MODULE

RV0500J01Z

规格书

Version 1.0 (EN)

PRODUCT SPECIFICATIONS

For Custome	er:	:APPROV	PROVALFOR SPECIFICATION				
Customer M	odel No		☐ :APPROVALFOR SAMPLE				
Module No.:	RV0500)J01Z	Date : 201	5.10.10			
			Version :1	.0			
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Approve	d By		Comment				
PREPAI	RED	CHECKED	VERIFIED BY QA DEPT	VERIFIED BY R&D DEPT			
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2. Revision Record

Date	Rev.No.	Page	Revision Items	Prepared
2015.10.10	1.0		The first release	

3. General Specifications

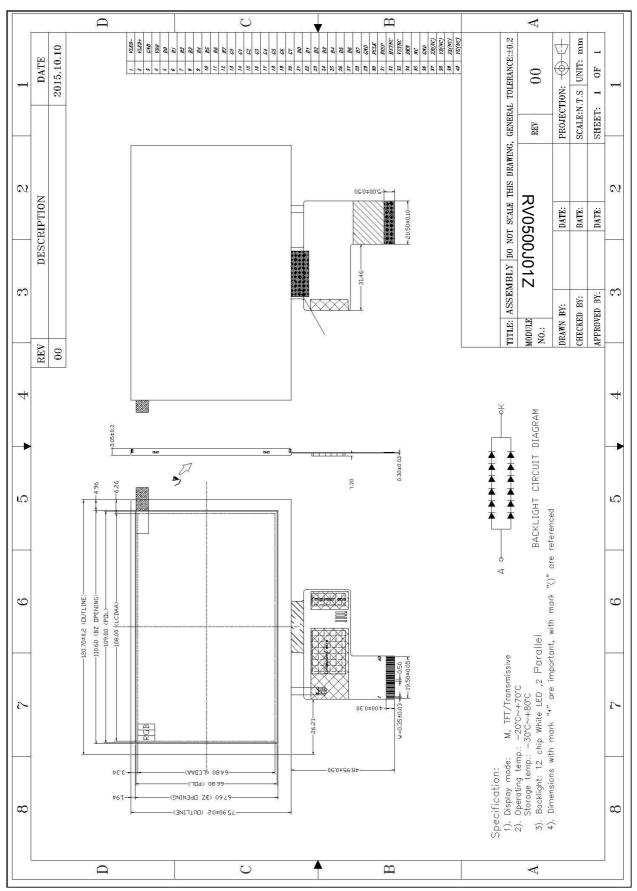
RV0500J01Z is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC, a back light unit. The $5.0^{\prime\prime}$ display area contains 800×480 pixels and can display up to 16.7M colors. This product accords with RoHS environmental criterion.

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M		1
Viewing Direction	6	O'Clock	
Operating temperature	-20~+70	°C	
Storage temperature	-30~+80	°C	
Module size	Refer to outline drawing	mm	2
Active Area(W×H)	108×64.8	mm	
Number of Dots	800×RGB×480	dots	
Drive IC	HX8264/HX8664	-	
Power Supply Voltage	3.3	V	
Outiline Dimensions	Refer to outline drawing	-	
Backlight	12-LEDs (white)	pcs	
Weight		g	
Data Transfer	RGB	-	

Note 1: Color tune is slightly changed by temperature and driving voltage.

Note 2: Without FPC and Solder.

4. Outline. Drawing



5. Absolute Maximum Ratings(Ta=25°C) Electrical Absolute Maximum Ratings.(Vss=0V ,Ta=25°¢

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	VDD	-0.3	3.6	V	
Input Voltage	V _{In}	-0.3	VDD+0.5	V	1, 2
Current of LED	ILED	0	20	mA	

Notes:

- If the module is above these absolute maximum ratings. It may become permanently damaged.
 Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
- 2. VDD>V_{SS} must be maintained.
- 3. Please be sure users are grounded when handing LCD Module.

Environmental Absolute Maximum Ratings.

ltem	Stor	age	Operat	Note			
item	MIN.	MAX.	MIN.	MAX.	Note		
Ambient Temperature	-30 _℃	80 _{°C}	-20 _℃	70 _℃	1,2		
Humidity	-	-	-	-	3		

- 1. The response time will become lower when operated at low temperature.
- 2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

3. Ta<=40°C85%RH MAX.

Ta>=40°CAbsolute humidity must be lower than the humidity of 85%RH at 40°C

6. Electrical Specifications and Timing Characteristics

Electrical characteristics(Vss=0V,Ta=25°¢

Parame	Parameter Symbol		Condition	Min	Тур	Max	Unit	Note
Power su	pply	VDD	Ta=25°C	2.7	3.3	3.6	V	
Input	'H'	V _{IH}	VDD=3.3V	0.7VDD	-	VDD	V	
voltage	'L'	V _{IL}	VDD=3.3V	0	-	0.3VDD	V	
Curren	ıt	I _{CC1}	Normal mode	-	-	-	mA	2
Consump	tion	I _{CC2}	Sleep mode	-	0.03	0.09	mA	2

Note:

- 1:When an optimum contrast is obtained in transmissive mode.
- 2: Tested in 1×1 chessboard pattern.

LED backlight specification(VSS=0V ,Ta=25°¢

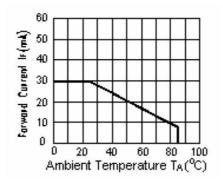
Item		Symbol	Condition	Min	Тур	Max	Unit	Note
Supply	voltage	_	-	_	19.2	_	V	1
Supply	current	I _f	-	_	40	_	mA	2
Forward	Normal	I _{pn}	6-chip	_	40	-	A	
current	Dimming	I _{pd}	series x 2	-	-	-	mA	

Note:

: VLED=VLED(+)-VLED(-). 2:The current of LED is 20mA.

A LED drive in constant current mode is recommended.

3: LED power consumption is around 0.768W.



Interface signals

Pin	Symbol	Description.
1	LED_K	Backlight LED Ground
2	LED_A	Backlight LED Power
3	GND	GND
4	VDD	Power supply
5~12	R0-R7	Red data bus
13~20	G0-G7	Green data bus
21~28	B0-B7	Blue data bus
29	GND	GND
30	PCLK	Data clock
31	DISP	Standby mode select pin
32	HSYNC	Line SYNC signal
33	VSYNC	Frame SYNC signal
34	DE	Data Enable Input
35	NC	NC
36	GND	GND
37	X1	Touch Panel Control Pin
38	Y1	Touch Panel Control Pin
39	X2	Touch Panel Control Pin
40	Y2	Touch Panel Control Pin

Timing Characteristics

Horizontal timing

Parameter	Symbol		Spec.	Spec.				
raiailletei	Syllibol	Min.	Тур.	Max.	Unit			
Horizontal Display Area	thd		800		DCLK			
DCLK frequency	fclk	-	30	50	MHz			
One Horizontal Line	th	862	1056	1200	DCLK			
HS pulse width	thpw	1	1-1	40 🗸	DCLK			
HS Back Porch (Blanking)	thb		46		DCLK			
HS Front Porch	thfp	16	210	354	DCLK			
DE mode Blanking	th-thd	85	256	400	DCLK			

Vertical timing

Parameter	Symbol		Unit		
raiailletei	Syllibol	Min.	Тур.	Max.	Offic
Vertical Display Area	tvd		480		T _H
VS period time	tv	513	525	650	T _H
VS pulse width	tvpw	3	O.	(20)	T _H
VS Back Porch (Blanking)	tvb	5//	23	100	T _H
VS Front Porch	tvfp	7	22 /	147	T _H
DE mode Blanking	tv-tvd	30	45	170	T _H

Parallel 24-bit RGB mode

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
CLKIN Frequency	Fclk	-	40	50	MHz	VDD=3.0V~3.6V
CLKIN Cycle Time	Tclk	20	25	-	ns	
CLKIN Pulse Duty	Tcwh	40	50	60	%	Tclk
Time from HSD to Source Output	Thso		64		CLKIN	- /// -
Time from HSD to LD	Thld		64		CLKIN	2
Time from HSD to STV	Thstv		2		CLKIN	- 70
Time from HSD to CKV	Thckv		20		CLKIN	
Time from HSD to OEV	Thoev		4		CLKIN	•
LD Pulse Width	Twld		10	1	CLKIN	-
CKV Pulse Width	Twckv		66		CLKIN	1*1
OEV Pulse Width	Twoev		74	(02)	CLKIN	•

7. Optical Characteristics

Item	Sy	mbol	Condition	Min.	Тур.	Max.	Unit	Note
Brightness	I	Зр	<i>θ</i> =0°	-	220	-	Cd/m ²	1
Uniformity	4	∄ p	Ф=0°	75	80	_	%	1,2
	3	:00		-	60	-		
Viewing	6	:00	Cr≥10	-	60	-	D	_
Angle	9	:00	CIZIU	-	65	-	Deg	3
	12	2:00		-	50	-		
Contrast Ratio		Cr	<i>θ</i> =0°	-	350		-	4
Response Time	Т	r+T _f	Ф=0°		20		ms	5
		Х		0.26	0.31	0.36	-	
	W	у		0.28	0.33	0.38	-	
		Υ		-	-	-		
		Х					_	
Oalan af	R	у					_	
Color of CIE		Υ		-	-	-		
Coordinate		Х	<i>θ</i> =0°					1,6
	G	у	Ф=0°					1,0
		Υ		-	-	-		
		Х					-	
	В	у					-	
		Υ		-	-	-		
NTSC Ratio		S		-	50	-	%	

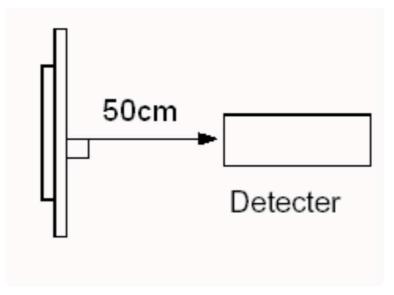
Note: The parameter is slightly changed by temperature, driving voltage and materiel

Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment PR-705 (Φ8mm)

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: Ta=25°C.
- Adjust operating voltage to get optimum contrast at the center of the display.

Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.

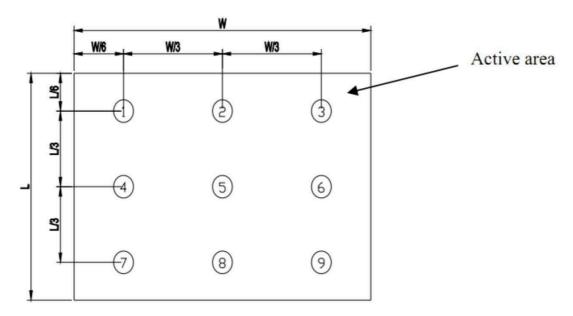


Note 2: The luminance uniformity is calculated by using following formula.

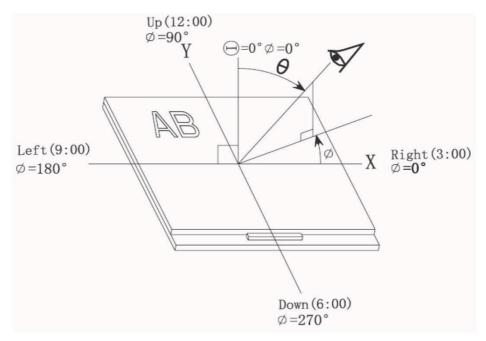
 \triangle Bp = Bp (Min.) / Bp (Max.)×100 (%)

Bp (Max.) = Maximum brightness in 9 measured spots

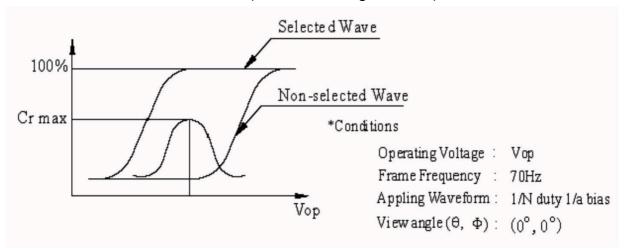
Bp (Min.) = Minimum brightness in 9 measured spots.



Note 3: The definition of viewing angle: Refer to the graph below marked by θ and Φ



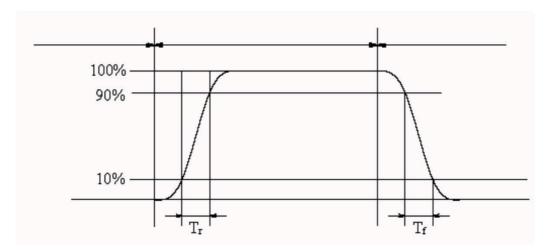
Note 4: Definition of contrast ratio.(Test LCD using DMS501)



Contrast ratio (Cr) =
$$\frac{Brightness \ of \ selected \ dots}{Brightness \ of \ non-selected \ dots}$$

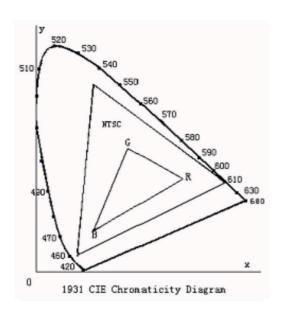
Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.

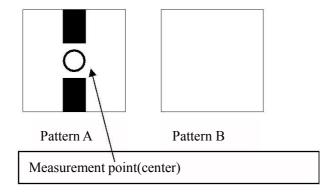


Color gamut:

$$S = \frac{area \ of \ RGB \ triangle}{area \ of \ NTSC \ triangle} \times 100\%$$

Note 7: Definition of cross talk.

Cross talk ratio(%)=|pattern A Brightness-pattern B Brightness|/pattern A Brightness*100



Electric volume value=3F+/-3Hex

8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion
1	High Temperature Storage	80°€2°C96H Restore 2H at 25°C Power off	
2	Low Temperature Storage	-30°€2°C96H Restore 2H at 25°C Power off 70°€2°C96H	1. After testing,
3	High Temperature Operation	Restore 2H at 25°C Power on -20°€2°C96H	cosmetic and electrical defects should not happen.
4	Low Temperature Operation	Restore 4H at 25°C	Total current consumption should
5	High Temperature/Humidity Operation	60°€2°C90%RH 96H Power on -30°€	not be more than twice of initial value.
6	Temperature Cycle	30min 5min 30min after 5 cycle, Restore 2H at 25°C Power off	
7	Vibration Test	10Hz~150Hz, 100m/s², 120min	Not allowed cosmetic
8	Shock Test	Half- sine wave,300m/s ² ,11ms	and electrical defects.
9	ESD Test	Air discharge:+/-8KV, Contact discharge:4KV	

Note: Operation: Supply 3.3V for logic system.

The inspection terms after reliability test, as below

ITEM	Inspection
Contrast	CR>50%
IDD	IDD<200%
Brightness	Brightness>60%
Color Tone	Color Tone+/-0,05

9 Quality level

Classification of defects

Major defects (MA): A major defect refers to a defect that may substantially

degrade usability for product applications, including all functional defects(such as no display, abnormal display, open or missing segment, short circuit, missing component), outline dimension beyond the drawing, progressive defects and those affecting reliability.

Minor defects (MI): A minor defect refers to a defect which is not considered to be able to substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation, such as black spot, white spot, bright spot, pinhole, black line, white line, contrast variation, glass defect, polarizer defect, etc.

Definition of inspection range

For dot defect of TFT LCD which is not smaller than 3 inches, dividing three areas to make a judgment (according to figure 1).

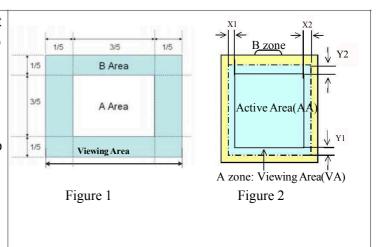
A area: center of viewing area
B area: periphery of viewing area

C area: Outside viewing area

For other defects, dividing two areas to make a judgment (according figure 2).

A zone : Inside Viewing area B zone : Outside Viewing area

X1(A.A~V.A): 2mm X2(A.A~V.A): 2mm Y1(A.A~V.A): 2mm Y2(A.A~V.A): 2mm



Inspection items and general notes

1114	inspection items and general notes					
General notes	1.Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and TIANMA. 2.Viewing area should be the area which TIANMA guarantees. 3.Limit sample should be prior to this Inspection standard. 4.Viewing judgment should be under static pattern. 5.Inspection conditions Inspection distance: 250 mm (from the sample) Temperature : 25±5 °C Inspection angle : 45 degrees in 6 o'clock direction (all defects in viewing area should be inspected from this direction)					
	Pinhole, Bright spot, Black spot,	T1 1 6 11 11 11 11 11 11 11 11 11 11 11 1				
	White spot, Black line, White Line, Foreign particle, Bubble	The color of a small area is different from the remainder. The phenomenon doesn't change with voltage				
Inomostica	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage				
Inspection items	Polarizer defect	Scratch, Dirt, Particle, Bubble on polarizer or between polarizer and glass				
	Dot defect (TFT LCD)	The pixel appears bright or dark abnormally when display				
	Functional defect	No display, Abnormal display, Open or missing segment, Short circuit, False viewing direction				

Glass defect	Glass crack, Shaved corner of glass, Surplus glass
PCB defect	Components assembly defect

Outgoing Inspection level

Outgoing Inspection	Inspection conditions	Inspection				
standard			Max.	Unit	IL	AQL
Major Defects	See 8.3 general notes	5	See 8.	5	Ш	0.065
Minor Defects	See 8.3 general notes	S	See 8.	5	II	0.065
Note: Sampling standard conforms to GB2828						

Inspection Items and Criteria

				Judgment standard				
	Inspection items			Category	Acceptable number			
			Category		A zone	B zone		
			Α	Ф<=0.20	Neglected	Neglected		
	Black spot, White spot,	b 1	В	0.20<Ф<=0.25	3	Neglected		
1	Pinhole, Foreign Particle, Particle	a	С	0.25<Ф<=0.3	2	Neglected		
'	in or on glass, Scratch on glass	Ф=(a+b)/2(mm	D	0.3<Ф<=0.4	1	3		
	Coratori ori glass	(// - 2.5)	E	0.4<Ф<=0.5	0	2		
		(a/b<2.5)	Tot	al defective point(B,C)	1	-		
		4	Α	W<=0.03	Neglected	Neglected		
		line, and Particle Between Polarizer and	В	0.03 <w<=0.05 L<=3.0</w<=0.05 	3	Neglected		
2	Black line, White line, and Particle Between		С	0.05 <w<=0.1 L<=3.0</w<=0.1 	2	Neglected		
	glass, Scratch on		D	0.05 <w<=0.1 L<=4.0</w<=0.1 	1	3		
	giuoo		Е	W>0.1 L>4.0	0	2		
			Total defective point(B,C)		1	-		
3	Bright spot		any size		none	none		
4	Contrast		Α Φ<0.2		Neglected	Neglected		

	variation		В	<Ф<=0.3	2	
	variation	*	С	<Ф<=0.4	1	
		b	D	0.4<Ф	0	
		$\begin{array}{ c c } \hline & a \\ \hline & \Phi = (a+b)/2 \text{(mm)} \\ \hline \end{array}$	То	tal defective point(B,C)	3	
5	Bubble inside cell			any size	none	none
	Polarizer defect	Scratch ,damage on polarizer, Particle on polarizer or between polarizer and glass.	Ref	fer to item 1 and item 2.		
6	(if Polarizer is used)	Bubble, dent and convex	Α	Ф<=0.1	Neglected	Neglected
	·		В	0.1 <Ф<=0.2	2	Neglected
			С	0.2 <Ф<=0.3	1	2
7	Surplus glass	Stage surplus glass Surrounding surplus glass		=0.3mm ould not influence outline	dimension and as	sembling.
8	Open segment or o	open common	Not	permitted		
9	Short circuit		Not	permitted		
10	False viewing direc	ction	Not	permitted		
11	Contrast ratio uneven		According to the limit specimen			
12	Crosstalk			According to the limit specimen		
13	Black /White spot(display)			Refer to item 1		
14	Black /White line(display)			fer to item 2		

			Judgment standard			
	Inspection items			Category(application: B zone)	Acceptable number	
		i) The front of lead terminals	В	a≤t, b≤1/5W, c≤3mm Crack at two sides of lead terminals should not cover patterns and alignment mark		
	Glass	Inner border line of the seal Outer border line of the seal	b < Inner borderline of the seal		Max.3	
15	defect crack	Inner border line of the seal Outer border line of the seal		COuter borderline of the seal	defects	
		iv) Corner	В	a <= t, b <= 3.0, c <= 3.0 Glass crack should not cover patterns u and alignment mark and patterns.		

		Inspection items	Judgment standard
		inspection terms	Category(application: B zone)
16	PCB defect	Component soldering: No cold soldering short open circuit burn tin ball The flat encapsulation component position deviation must be less than 1/3 width of the pin (Pic.1); the sheet component deviation: Pin deviates from the pad and contact with the near components is not permitted (Pic.2) lead defect: The lead lack must be less than 1/3 of its width; The lead burr must be less than 1/3 of the seam; Impurities connect with the near leads is not permitted	Component Soldering pad Lead L2>0 L1>0
		Connector soldering: Soldering tin is at contact position of the plug and socket is not permitted No foundation is scald Serious cave distortion on plug and socket contact pin is not permitted	Soldering tin is not permit in this area Soldering tin is not permit in this area Socket Base Board Base Board

	Glue on root of the speaker receiver and motor lead: The insulative coat of the lead must join into the PCB; the protected glue must envelop to the insulative coat.	Glue Lead PCB Insulative coat
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10. Precautions for Use of LCD Modules

10.1 Handling Precautions

The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer.

Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

Do not attempt to disassemble the LCD Module.

If the logic circuit power is off, do not apply the input signals.

To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- a. Be sure to ground the body when handling the LCD Modules.
- b. Tools required for assembly, such as soldering irons, must be properly ground.
- c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0° C $\sim 40^{\circ}$ C

Relatively humidity: ≤80%

The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

